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AMENDMENTS TO THE CLAIMS

Please add or amend the claims to read as follows, and cancel without prejudice or disclaimer to resubmission in a divisional or continuation application claims 1-29 indicated as cancelled:

1-29. (Cancelled)

30. (New) A circuit for receiving an input signal and providing an output signal comprising:

a first circuit branch including a first amplifying element, said first amplifying element including a Class D amplifier, for amplifying the input signal when the frequency is below a pre-determined frequency;

a second circuit branch connected in parallel with said first circuit branch and including a second amplifying element for amplifying the input signal when the frequency is above a pre-determined frequency; and

a power controller connected to the Class D amplifier to control the power supplied to the Class D amplifier,

wherein said power controller supplies substantially constant voltage to said class D amplifier substantially irrespective of the current sourced or sinked within an operating range of power.

31. (New) A circuit as in Claim 30 wherein the second amplifying element includes an amplifier selected from the group consisting of Class A, Class B and Class AB amplifier.

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32. (New) A circuit as in Claim 30, wherein the second circuit branch includes a high pass filter.
33. (New) A circuit as in Claim 32, wherein the high pass filter comprises a capacitor.
34. (New) A circuit as in Claim 30 wherein the first branch comprises a low pass filter.
35. (New) A circuit as in Claim 34, wherein the low pass filter comprises an inductor.
36. (New) A circuit as in Claim 32, wherein the first branch comprises a low pass filter.
37. (New) A circuit as in Claim 36, wherein the low pass filter comprises an inductor.
38. (New) A circuit as in Claim 30, wherein a feedback signal corresponding to the sum of a signal produced by the first circuit branch and a signal produced by the second circuit branch is mixed with the input signal.
39. (New) A circuit as in Claim 30, wherein the input signal comprises a horizontal deflection signal for a cathode ray tube.
40. (New) A circuit as in Claim 30, wherein the input signal comprises a vertical deflection signal for a cathode ray tube.

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41. (New) A circuit as in claim 30, wherein said second amplifying element of said second circuit branch has a cut-off frequency, and wherein said cut-off frequency may be adjusted.

42. (New) A circuit as in claim 30, wherein said cut-off frequency may be variably adjusted.

43. (New) A circuit as in claim 30, wherein the power controller is capable of supplying substantially constant voltage substantially irrespective of the current sourced or sinked within an operating range of power to a load, said power controller comprising:

- a voltage source to provide a voltage between first and second terminals;
- a pulse width modulation controller connected to receive at least a portion of the voltage between said first and second terminals of said voltage source and to produce an output;
- an inductor having a first terminal connected to the output of the pulse width modulation controller and a second terminal connected to ground;
- a first capacitor connected between the first terminal of the voltage source and ground; and
- a second capacitor connected between the second terminal of the voltage source and ground, wherein the class D amplifier is supplied with voltage between the first terminal of the voltage source and ground.

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44. (New) A circuit as in claim 43, wherein the class D amplifier is supplied with voltage between the second terminal of the voltage source and ground.

45. (New) A method of amplifying an electric signal comprising:
 amplifying the electric signal predominantly using a first amplifying element, said first amplifying element including a Class D amplifier, when the frequency of the signal is below a pre-determined frequency;
 amplifying the electric signal predominantly using a second amplifying element when the frequency of the signal is above the pre-determined frequency; and
 supplying said Class D amplifier with substantially constant voltage substantially irrespective of the current sourced or sinked within an operating range of power.

46. (New) The method of Claim 45 wherein the second amplifying element includes an amplifier selected from the group consisting of Class A, Class B and Class AB amplifier.

47. (New) The method of Claim 46 wherein the electric signal includes a horizontal deflection signal in a cathode ray tube.

48. (New) The method of Claim 46 wherein the electric signal includes a vertical deflection signal in a cathode ray tube.

49. (New) The method of claim 45, further comprising:

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adding to the electric signal a second electric signal corresponding to the sum of the signals produced by the first and second amplifying elements.

50. (New) Apparatus for supplying substantially constant voltage substantially irrespective of the current sourced or sinked within an operating range of power to a load comprising:

a voltage source to provide a voltage between first and second terminals;
a pulse width modulation controller connected to receive at least a portion of the voltage between the first and second terminals of said voltage source and to produce an output;
an inductor having a first terminal connected to the output of the pulse width modulation controller and a second terminal connected to ground;
a first capacitor connected between the first terminal of the voltage source and ground; and
a second capacitor connected between the second terminal of the voltage source and ground,
wherein the voltage between the first terminal of the voltage source and ground is substantially constant.

51. (New) Apparatus in claim 50, wherein the voltage between the first terminal of the voltage source and ground is adapted to supply power to a class D amplifier.

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52. (New) Apparatus in claim 51, wherein the voltage between the second terminal of the voltage source and ground is adapted to supply power to a class D amplifier.